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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 09/824,225 04/03/2001 PAT 6,724,019

YES

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2000-159544 05/25/2000

YES

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 03/30/2004

| Foreign Priority claimed<br>35 USC 119 (a-d) conditions met<br>Verified and Acknowledged | yes <input checked="" type="checkbox"/> no <input type="checkbox"/><br>Met after<br>allowance<br>Initials<br>Examiner's Signature | STATE OR COUNTRY<br>JAPAN | SHEETS<br>DRAWING<br>37 | TOTAL CLAIMS<br>12 | INDEPENDENT CLAIMS<br>1 |
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## TITLE

Semiconductor device and manufacturing method

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